

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Michael H. Perrott, Rex T. Baird, Yunteng Huang
Title: DIGITALLY-SYNTHESIZED LOOP FILTER CIRCUIT
PARTICULARLY USEFUL FOR A PHASE LOCKED LOOP
Application No.: Not Yet Assigned Filed: Herewith
Examiner: Not Yet Assigned Group Art Unit: Not Yet Assigned
Atty. Docket No.: 026-0002-1

September 5, 2003

Mail Stop Patent Application
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**INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. § 1.97**

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Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the undersigned brings the patents, publications, applications or other information identified in the attached:

- ☒ Form(s) PTO-1449
- ☒ Copy of International Search Report for PCT/US01/21645
- ☒ Copy of International Search Report for PCT/US01/21644

to the Examiner's attention in the above-identified application. **These references were cited in parent Application No. 09/902,541, filed July 10, 2001. Accordingly, in accordance with C.F.R. §1.98(d), copies of references are not being supplied herewith.** Citation of such information shall not be construed as:

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- ☒ This Information Disclosure Statement is filed within three months of the filing date of a national application other than a continued prosecution application under § 1.53(d) or within three months of entry of the national stage as set forth in § 1.491 in an international application. Therefore, no fee is required.
- ☐ The undersigned believes that this Information Disclosure Statement is being filed before the mailing date of a first Office action on the merits or before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Therefore, no fee is believed required.

If however, this Information Disclosure Statement is filed after the period specified in § 1.97(b), the undersigned hereby authorizes the Commissioner to charge the fee set forth in § 1.17(p) to Deposit Account No. 50-0631.

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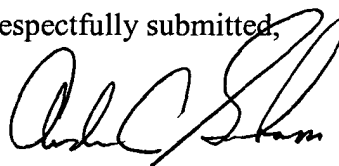
I hereby certify that, on the date shown below, this correspondence is being

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Date

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Respectfully submitted,



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U.S. Department of Commerce, Patent and Trademark Office				Attorney Docket No.: 026-0002-1		
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(Use several sheets if necessary)				Filing Date: Herewith		
				Group Art Unit: Not Yet Assigned		
				Date Submitted: September 5, 2003		
U.S. Patent Documents						
*Examiner Initial		Document Number	Date	Name		
	AA	6,150,891	Nov. 21, 2000	Welland et al.		
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	AJ					
	AK					
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		Document	Date	Country	Yes	No
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	AR	Andersson, L. I. et al, "Silicon Bipolar Chipset for SONET/SDH 10 Gb/s Fiber-Optic Communication Links," IEEE Journal of Solid-State Circuits, Vol. 30, No. 3, Mar. 1995, pp. 210-218.				
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<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>						

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NON PATENT LITERATURE DOCUMENTS		
*Examiner Initial	Cite No.	(Including name of author in capital letters, title of article, title of item, date, pertinent pages, volume-issue number(s), publisher, city and/or country where published.)
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	AA	3,968,493	Jul. 6, 1976	Last et al.		
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